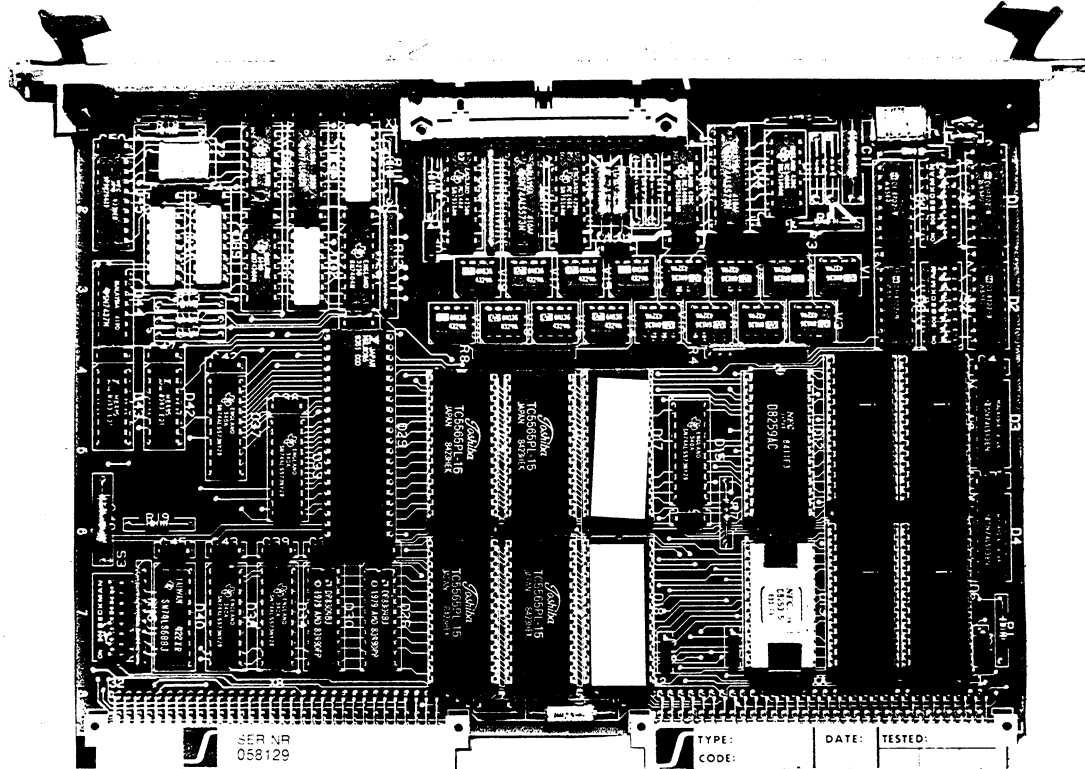


**DMC COMMUNICATION CONTROL**



**3100-US1 SERIES A**

**DESCRIPTION**

3100-US1 (USART86-CONTB) is an intelligent asynchronous communication module for controlling four independent serial channels with 8 protocol baud rates to 19,200. The board contains an 8086 processor, 16 byte EPROM memory, 16K byte shared RAM memory and 16 byte local RAM memory in addition to an interrupt and a timer. Communication with the 3100-CPU is via shared memory.

**INDICATORS**

RUN  
BLD

**ASSOCIATED FUNCTIONAL BLOCKS**

COMINI  
USDIAL  
PROT 1, 2, 3, 4, 5, 8

**SPECIFICATIONS**

Location:	CPU rack
Power Requirements:	5V @ 1.4A.
Environment:	Temperature: 0 to 50°C Humidity: 5 to 95%

**SELECTIONS**

Board #  
Channel baud rates

**CONNECTIONS AND ASSOCIATED PRODUCTS**

3100-US2  
3130-UT  
3130-SC2  
3130-SC1

**Switch Locations and Settings**

The USART86-CONT reserves a 16K byte area in the 3100-CPU memory which is set with DIP switch S3 (Figure 1). The rates of transmission (Table 2) are selected using Switches S1 and S2.

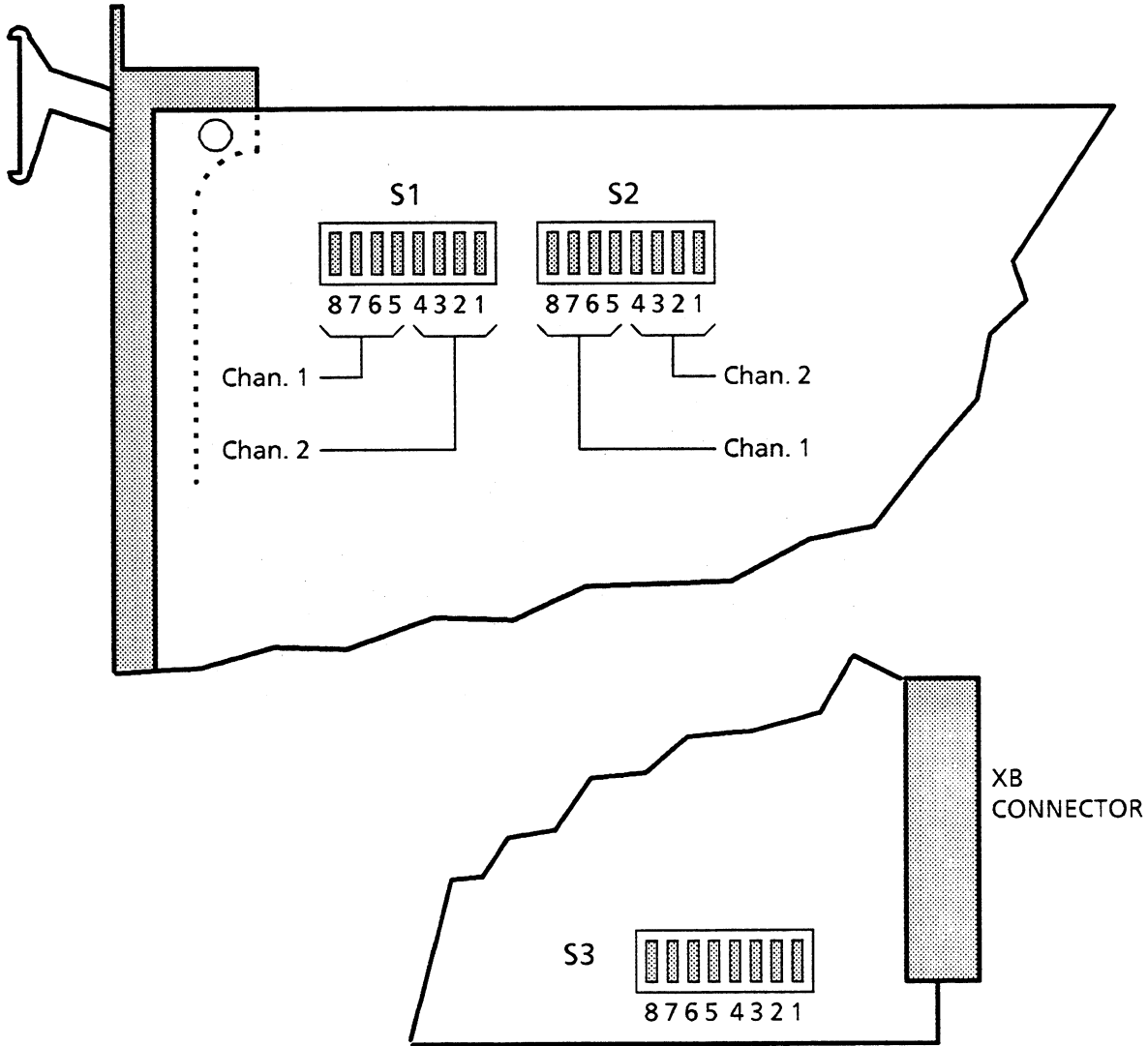


Figure 1 Switch Locations on the USART86-CONT Board

**Interconnection**

The connection of 3100-US1 board can be made using terminal strip board 3130 - UT or 3130 - SCI. Figure 2 shows an example of the 3100-US1 connected to a CRT terminal using the 3130 - SCI terminal strip board.

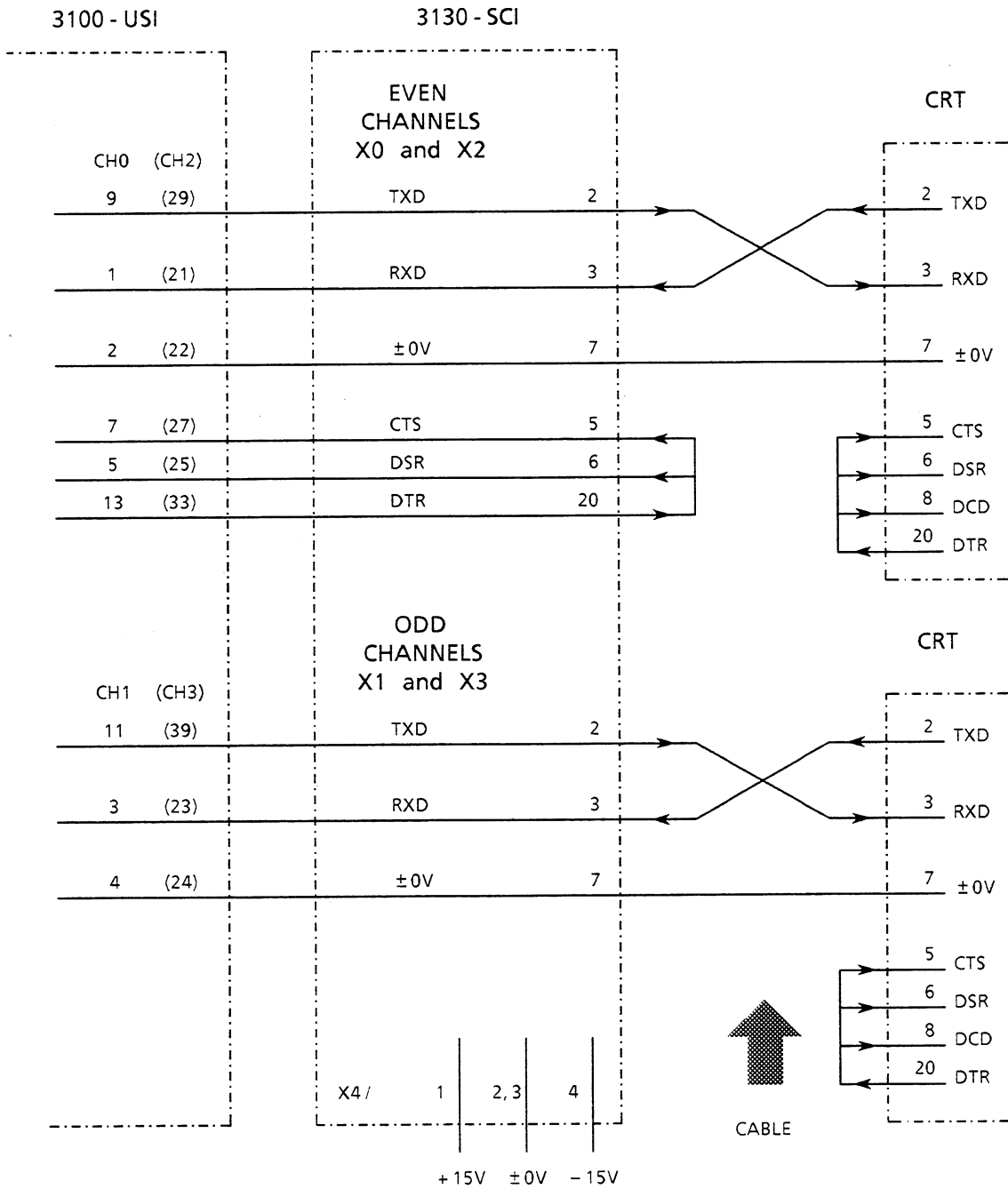


Figure 2. 3100-US1 terminal Connection via a 3130 - SCI Board.