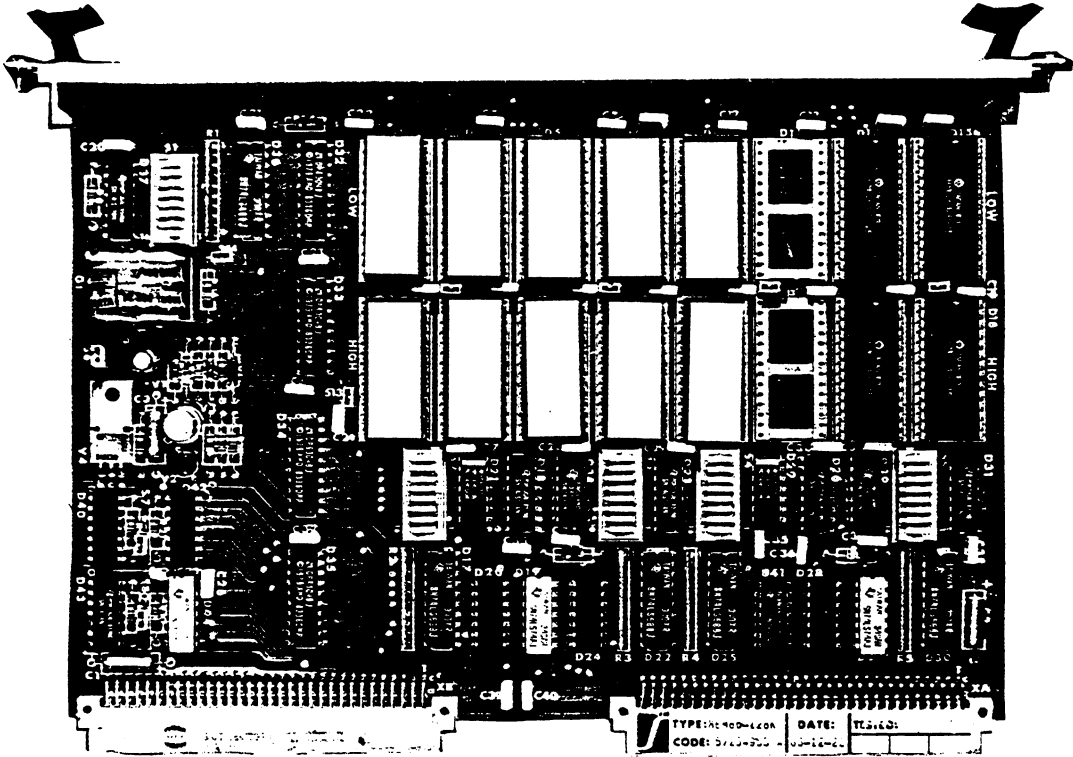


**DMC SYSTEM MEMORY**



**3100 - MS1 Series A**

**DESCRIPTION**

3100-MS1 (MEM86-128K/CLI) is the system memory for the Digital Master Control when used with the 3100 - SM shared memory board.

**INDICATORS**

None

**SELECTIONS**

None

**SPECIFICATIONS**

Location:	CPU rack
Power Requirements:	5V @ 1A
Environment:	Temperature: 0 to 50°C Humidity: 5 to 95%

**CONNECTIONS AND ASSOCIATED PRODUCTS**

3100-MS2 ,3100-MS2 and 3100-SM

**ASSOCIATED FUNCTIONAL BLOCKS**

None

Switch Settings & Locations

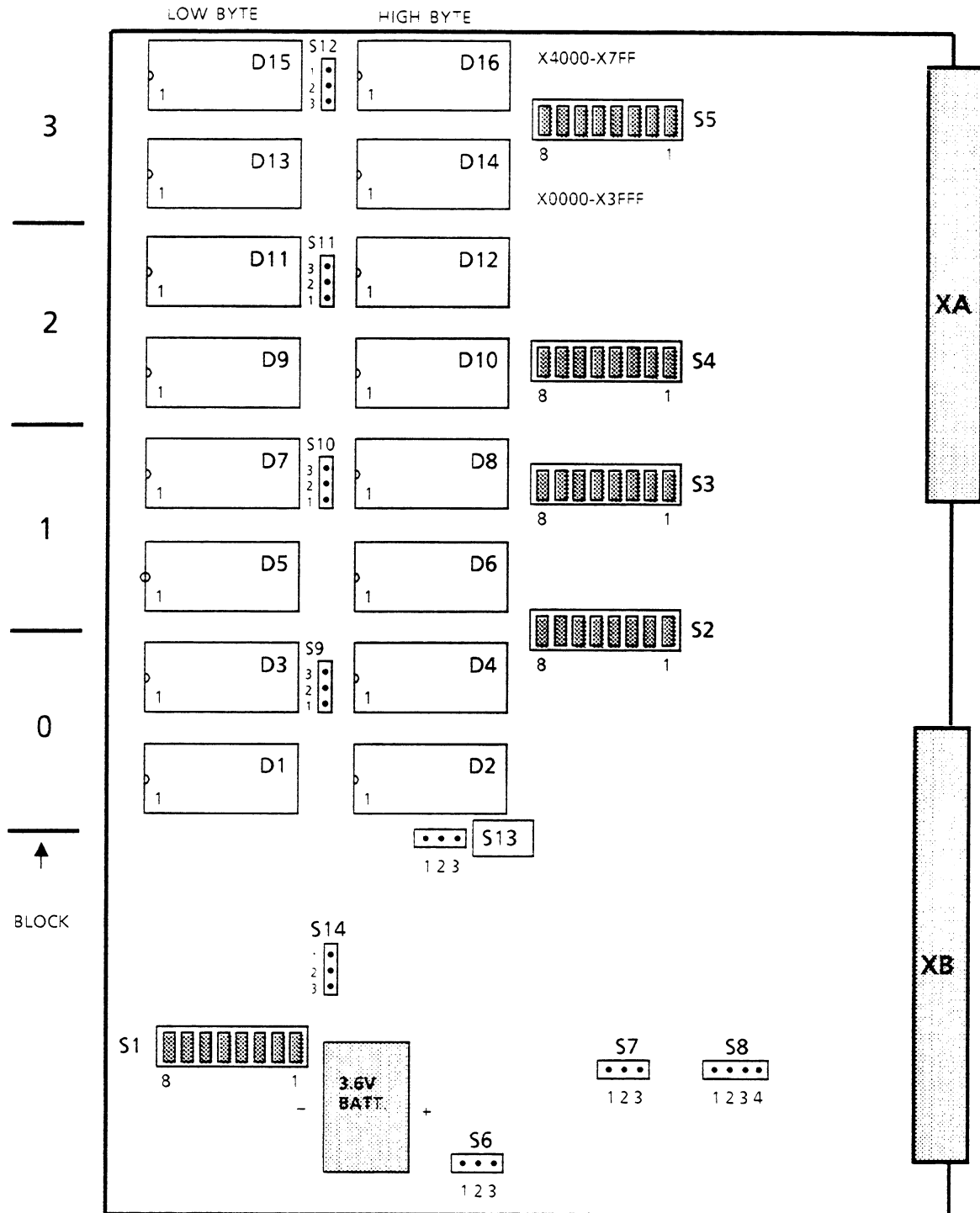


Figure 1. Location of the Memories and Switches on the 3100 - MS1 Board

Table 1. Factory Switch Settings

Switch	Setting
S1	X (Not Significant)
	<u>8 7 6 5 4 3 2 1</u> -----
S2	0 0 0 0 0 0 1 1    Logic 1 = Switch Open
S3	0 0 0 0 1 0 1 1    Logic 0 = Switch ON
S4	0 0 0 1 0 0 1 1
S5	1 1 0 1 0 0 1 1
S6	2-3 (battery connected)
S7	1-2 (fixed memory)
S8	1-2 (memory cycle extension)
S9	1-2 (Voltage for block 0 from buss)
S10	1-2 (Voltage for block 1 from buss)
S11	2-3 (Voltage for block 2 from battery)
S12	2-3 (Voltage for block 3 from battery)
S13	2-3 Voltage Supervision circuit from Buss
S14	1-2 Secured power from board